### **ABOUT VIT**

VIT was founded in 1984 as Vellore Engineering College by the Chancellor, Dr. G. Viswanathan. From its humble beginnings, the institution has grown exponentially to that of more than 35,000 students. It was conferred the University status in 2001 in recognition of its excellence in academics, research and extracurricular initiatives. Currently, VIT has 4 campuses — in Vellore, Chennai, Amaravati (AP) and Bhopal (MP). VIT has been consistently ranked among the best institutions of the country, and is aspiring to emerge as a global leader. The National Institutional Ranking Framework (NIRF) of the MHRD, Government of India, has identified VIT as the best Private Engineering Institution in India.

With students from all the states of India and from more than 50 countries, the cosmopolitan VIT provides an appropriate ambience for holistic learning and comfortable living. Sports, games and cultural activities are an integral part of student life on campus. VIT holds an exemplary placement record by consistently placing more than ninety percentage of the students in good companies. The VIT's international linkages provide ample opportunities for students and faculty to gain global exposure. VIT alumni, spread across the world, are serving the most advanced as well as the most deprived societies.

#### **ABOUT School**

School of Electrical Engineering (SELECT) has 101 faculty members who have done their UG and PG degrees from the top-notch universities. The School offers B.Tech (Electrical and Electronics Engineering), B.Tech (Electronics and Instrumentation Engineering), M.Tech (Power Electronics and Drives) and M. Tech (Control and Automation), M.S. by Research and Ph.D. in Engineering. B.Tech (Electrical and Electronics Engineering) and B.Tech (Electronics and Instrumentation Engineering), is accredited by the Engineering Accreditation Commission of ABET. All UG & PG programmes of the school are accredited by the Institution of Engineering and Technology (IET), U.K. The placement record of the School has always been impressive. Almost 100% of the students get job from the campus placement and many of them are getting it in core companies every year. The School has state-of-the art laboratories in almost all the areas of Electrical, Electronics and Instrumentation Engineering. Every year, students get scholarships to do their final year projects abroad under the Semester Abroad Program (SAP). Schneider Electric, India and NxP

Semiconductors, India, have established Centre of Excellence for students R&D activities under the guidance of faculty members and Industry experts. The School has signed MoUs with many foreign Universities, research organizations and Industries from where students get benefits for their R&D Work / Projects from the MoUs.

### **ABOUT THE VIRTUAL CONFERENCE**

The objective of the conference is to provide a professional forum to discuss and apply the most recent advancement in power systems, renewable energy and photonics domain pertaining to national and international level research. Eminent professors from Australia, Egypt and USA will be a part of this conference.

### **INVITED SPEAKERS**

## Dr. Akhtar Kalam

Professor & Discipline leader, Engineering Victoria University, Australia

## **Dr.Zubin Jacob**

Associate Professor of Electrical and Computer Engineering Purdue University, Indiana, USA.

# **Dr. Abdel-Moamen Mohammed**

Assistant Professor Faculty of Energy Engineering, Aswan University, Aswan, Egypt .

### **LIST OF TOPICS**

Recent advancement in power system operation and control Integration issues with Renewable energy sources Quantum science and engineering

Interested participants are asked to submit their research article and register to k.ravi@vit.ac.in [Registration Fee - 500 only]

Paper Presenter will get certificate and conference proceedings



India –USA- Australia – Egypt



Jointly organising a

One Day International Virtual Conference On

Recent Advancements in Power System, Renewable Energy and Photonics (RAPSREP'19)

Conference Date August 2, 2019

Organized by
School of Electrical Engineering,
Vellore Institute of Technology
Vellore, India

#### **PATRON**

Dr. G. Viswanathan, Chancellor

### **CO-PATRONS**

Dr. Sekar Viswanathan, Vice President
Mr. Sankar Viswanathan, Vice President
Mr. G.V. Selvam, Vice President
Dr. Sandhya Pentareddy, Executive Director
Dr. Anand A Samuel, Vice Chancellor
Dr. S. Narayanan, Pro-Vice Chancellor

#### **CONVENER**

Dr. S.Sivabalan
Dean, School of Electrical Engineering,
Vellore Institute of Technology, Vellore.

### **CO-CONVENER**

Dr. Meikandasivam, HOD, Dept. of Electrical Engineering School Electrical Engineering, VIT Vellore

### **CONFERENCE CO-ORDINATORS**

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