

Resource Persons:



Ganesan Narayanasamy, Education and Research Global Leader, IBM



Dr. Peter Hofstee, IBM Systems



Luke Leighton, Technology Leader, LibreSOC



Abhishek Sharma, Object Automation



Arjun Nag, Senior Design Verification lead, Tata Consultancy



Manikandan Nagarajan, Object Automation

Many more distinguished academic experts and Industry leaders will speak/present on designing SoC using IBM OpenPOWER cores.

Course Content:

- Multicore Computing and Parallel Programming
- POWER10 Architecture
- Unveiling OpenPOWER Course Introduction
- Microwatt Introduction
- System on Chip (SoC) and its Components & Introduction to IP Cores
- Testing concepts – Introduction
- Flow of the stimulus in IP verification environment
- Components of SoC verification

Course Fee:

- **Registration Fee: Rs. 1000/- (inclusive of GST)**

Registration Link:

- <https://events.vit.ac.in/>

Last date for Registration: 25th February 2022

Confirmation to the Participants: 1st March 2022

Timings: 9.30 a.m. – 5.30 p.m.

Mode: Online (Link will be shared to the registered participants)

Note:

- E-certificates will be provided for all the participants.
- Paid internship for the top three performers of the quiz conducted at the end of the workshop.

Coordinators

Dr. Prayline Rajabai C

Dr. Hariharan I

Department of Micro and Nano Electronics
School of Electronics Engineering



Three Day Online Workshop
on
SoC Design Using OpenPOWER
Cores
(3rd to 5th March 2022)



Organized by

Department of Micro and Nano Electronics
School of Electronics Engineering
Vellore Institute of Technology
Vellore, Tamil Nadu, India.

ABOUT VIT

VIT was established with the aim of providing quality higher education on par with international standards. It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis. The campus has a cosmopolitan atmosphere with students from all corners of the globe. Experienced and learned teachers are strongly encouraged to nurture the students. The global standards set at VIT in the field of teaching and research spur us on in our relentless pursuit of excellence. In fact, it has become a way of life for us. The highly motivated youngsters on the campus are a constant source of pride. Our Memoranda of Understanding with various international universities are our major strength. They provide for an exchange of students and faculty and encourage joint research projects for the mutual benefit of these universities. Many of our students, who pursue their research projects in foreign universities, bring high quality to their work and esteem to India and have done us proud. With steady steps, we continue our march forward. We look forward to meeting you here at VIT.

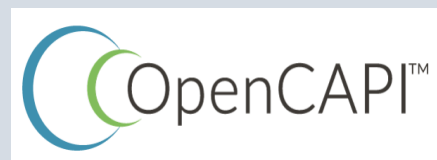
SCHOOL OF ELECTRONICS ENGINEERING

SENSE at VIT was established for imparting state-of-the-art knowledge in Electronics and Communication Engineering and allied areas. B.Tech., Electronics and Communication Engineering is accredited by the Engineering Accreditation Commission of ABET, <http://www.abet.org>. Students who are eligible are placed on campus and many of them are placed in core companies every year. The school has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nanotechnology. The latest simulation tools are used to cater to various specializations and are equipped with facilities for measurement, characterization and synthesis of experimental as well as theoretical results. Students are encouraged to take up their final year projects abroad too. The School has many industry sponsored advanced laboratories for carrying research and development. MoUs with many Foreign Universities,

Research Organizations and Industries facilitate student and faculty exchange. Faculty are actively involved in R&D activities and are working on research projects funded by government organisations like DRDO, ISRO (RESPOND), BRNS and agencies like DST.

ABOUT THE PROGRAM

The program is intended for the Faculty, UG and PG students, research scholars, practicing engineers and people from R & D in the field of VLSI Design. The main objective of the program is to provide basic concepts of SoC design, introduce OpenPOWER cores, VLSI testing and verification concepts. The course covers the concepts and tools you need throughout the entire VLSI pipeline. **You will earn a certificate on successful completion of this program and the top three performers will be provided with paid internship.**



Advisory Committee

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Dr. S. Kumaravel

Associate Professor & Head,

Department of Micro & Nano Electronics, SENSE

Coordinators

Dr. Prayline Rajabai C (Mobile:

7200577872, Email: prayline.c@vit.ac.in)

Dr. Hariharan I (Mobile: 7200703007, Email:

hariharan.i@vit.ac.in)

Department of Micro and Nano Electronics

School of Electronics Engineering

Vellore Institute of Technology, Vellore,

Tamil Nadu, India – 632014.