School of Electronics Engineering
Department of Micro and Nanoelectronics

National Workshop

on

"Technical writing and presentation with Latex"

(29th & 30th AUGUST, 2020)

About VIT

Vellore Institute of Technology (VIT) was founded in 1984 as Vellore Engineering College by the Chancellor Dr. G. Viswanathan. From its humble beginning, the institution has grown exponentially to that of having more than 33,000 students at present. Students from all over India and more than 50 countries are studying at VIT. University status was conferred in 2001 by MHRD Govt. of India in recognition of its excellence in academics, research and extracurricular initiatives. Currently, VIT has 4 campuses –in Vellore, Chennai, Amaravati (AP) and Bhopal (MP). The National Institutional Ranking Framework (NIRF) of the MHRD, Government of India, has identified VIT as the best Private Engineering Institution in India in the year 2016 and 2017. VIT has gone for accreditation by NAAC [India], IET [UK], and ABET [USA] and follows world class academic processes. VIT is the first and only University in India to get 4 star rating from QS, the world universities ranking organization. The Industry consortium FICCI, has declared VIT as the "University of the Year 2016", in India. VIT has also been ranked in the top 201 -250 in QS BRICS Ranking.

About SENSE

The School of ElectronicsEngineering (SENSE) emphasizes the fields of Electronics Engineering and Domain Specific Applications so as to facilitate the evolution of skills in students to help them attain a higher degree of knowledge, global competency and excellence, for the betterment of the society

Aims of the program

- Effective technical content preparation with a professional look
- Manuscript preparation compatible to standard journals
- Automatic placement of figures , graphs and tables in the manuscript
- Automatic referencing of figures , graphs and tables in the manuscript
- Developing references automatically and indexing them

Who Can Attend?
- Faculty, Research scholars , Students of any engineering discipline.

Course Content:
- Introduction to Latex
- Article template,
- page style , spacingsand Fonts
- Equations
- Indentation and enumeration
- Tables
- Figures
- Automatic referencing
- Bibliography

Registration for this Workshop is **FREE**.
Note: E-certificate will be providedto the participants

Chair Person:

Dr.Kittur Harish Mallikarjun
Professorand Dean,
School of Electronics Engineering (SENSE),
Vellore Institute of Technology, Vellore.

Convenor:

Dr. V.Arunachalam
Associate Professorand HoD
School of Electronics Engineering (SENSE), VIT Vellore

Coordinator

Dr. SriadibhatlaSridevi,
Associate Professor, SENSE
Prof. M.Aarthy,
Assistant Professor, SENSE,
VIT -Vellore

Resource Person:

Dr. Ravindra Dhuli
SENSE
VIT -Andhra Pradesh

Dr. Sriadibhatla Sridevi
Dr.Kishore V Krishnan,
SENSE
VIT -Vellore

Last date for Registration:
On or before : 28th August 2020

Registration Link:

https://forms.gle/XRn3ZArufApDEarG7

Link for downloading the software

 https://tinyurl.com/ztmz7j6