

28TH IEEE INTERNATIONAL SYMPOSIUM ON
VLSI DESIGN AND TEST


VDAT 2024

VIT Vellore, Tamil Nadu, India

**School Of Electronics Engineering
(SENSE)
Presents**

FPGA WORKSHOP
Pre-VDAT workshop

4th August, 2024

 VOC gallery - 2, Technology Tower



Guest Speaker

Mr. PADMANABAN K

Software Enabling & Optimization Engineer
CEG - Intel PSG

REGISTER AT:

<https://events.vit.ac.in/>

About VDAT

The VLSI Design & Test Symposium (VDAT) began as a workshop in 1998 and became a symposium in 2005 due to growing participation.

Since then, VDAT has been held annually, becoming a key event for VLSI professionals and academics. The symposium offers opportunities for academia, researchers, startups, and industry practitioners to share ideas, experiences, and knowledge in VLSI Design and Testing.

Over 27 years, this annual event has introduced many novel designs and technologies, contributing to global advancements in VLSI. High-quality technical paper presentations have guided participants towards new directions in VLSI technology.

In its 28th year, VDAT-2024 will be a three-day in-person event at the Vellore Institute of Technology (VIT), Vellore, Tamil Nadu, from September 1-3, 2024. VDAT-2024 is supported by the VLSI Society of India.

Who Can Attend ?

All VIT Undergraduate Students

Last Date for Registration

3rd August 2024

Event Fee

₹ 150 only

CERTIFICATES will be provided

Course Content

- Introduction to FPGA: Architecture & Operation
- FPGA Design Flow: Process of designing and implementing FPGA-based systems
- Hardware Description Language (Verilog or VHDL) for circuit design
- Hands-on Circuit Implementation FPGA boards.
- Problem-Solving and Critical Thinking through FPGA-based design challenges
- Foundation for Advanced Studies: Further exploration in FPGA-related fields.

Convener:

Dr. S. Sivanantham
Dean, School of Electronics Engineering (SENSE)

Co-Convener:

Dr. Jagannadha Naidu K
HoD, Department of Micro & Nano Electronics

Coordinators:

Dr. S. Ravi,
Associate Professor
Dr. Velmurugan V,
Associate Professor

For Registration contact:

Bhavesh Sai
Contact No.: 7989269410

Required Components will be provided