

About the Institute

Vellore Institute of Technology (VIT) was founded in 1984 as Vellore Engineering College by the Chancellor, Dr. G. Viswanathan. From its humble beginnings, the institution has grown exponentially to that of more than 35,000 students. It was conferred the University status in 2001 in recognition of its excellence in academics, research and extracurricular initiatives. Currently, VIT has 4 campuses – in Vellore, Chennai, Amaravati (AP) and Bhopal (MP). VIT has been consistently ranked among the best institutions of the country, and is aspiring to emerge as a global leader. The National Institutional Ranking Framework (NIRF) of the MHRD Government of India, has identified VIT as the best Private Engineering Institution in India. With students from all the states of India and from more 50 countries, the cosmopolitan VIT provides an appropriate ambience for holistic learning and comfortable living. Sports, games and cultural activities are an integral part of student on campus. VIT holds an exemplary placement record by consistently placing more than ninety percentage of the students in good companies. The VIT's international linkages provide ample opportunities for students and faculty to gain global exposure. VIT alumni, spread across the world, are serving the most advanced as well as the most deprived societies.

About School of Electrical Engineering

School of Electrical Engineering (SELECT) has 101 faculty members who

have done their UG and PG degrees from the top-notch universities. The School offers B.Tech (Electrical and Electronics Engineering), B.Tech (Electronics and Instrumentation Engineering), M.Tech (Power Electronics and Drives) and M. Tech (Control and Automation), M.S. by Research and Ph.D. in Engineering. B.Tech (Electrical and Electronics Engineering) and B.Tech (Electronics and Instrumentation Engineering), is accredited by the Engineering Accreditation Commission of ABET. All UG & PG programmes of the school are accredited by the Institution of Engineering and Technology (IET), U.K. The placement record of the School has always been impressive. Almost 100% of the students get job from the campus placement and many of them are getting it in core companies every year. The School has state-of-the art laboratories in almost all the areas of Electrical, Electronics and Instrumentation Engineering. Every year, students get scholarships to do their final year projects abroad under the Semester Abroad Program (SAP). Schneider Electric, India and NxP Semiconductors, India, have established Centre of Excellence for students R&D activities under the guidance of faculty members and Industry experts. The School has signed MoUs with many foreign Universities, research organizations and Industries from where students get benefits for their R&D Work / Projects from the MoUs.

About Value Added Program - VAC1710 - Exploring MATLAB-Simulink software and the Hands-on training

MATLAB - Simulink is a simulation software specifically designed for all engineering

disciplines. With fast simulation speed and friendly user interface, MATLAB-Simulink provides a powerful simulation environment to meet your simulation and development needs. MATLAB-Simulink provides a complete platform from design to simulation, to hardware implementation. In addition, MATLAB-Simulink links with the real-time software dSpace for closed control loop design. dSpace is designed specifically for automatic control, gate pulse generation for closed loop power electronic converters and control of electric drives and various engineering applications. This value added program is focused on helping young researchers to gain innovative idea and to advance the possibilities for Power Converters Design and Simulation like, hands-on training on types of power electronic converters, mathematical modelling of open loop system with average modelling, state space modelling design and stability analysis of closed loop converters, Renewable fed non-isolated and isolated converters are exploring.

Topics to be covered

- Introduction to MATLAB for power converters
- Analysis, design and Simulation of DC to DC converters using MATLAB
- Average mathematical modelling and state space modelling
- Closed loop control of power converter and stability analysis
- Simulation of renewable fed power converters using MATLAB.



VIT[®]
Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)

Five Day Value Added Program
On

**Exploring MATLAB-Simulink –
Hands-on Training**
September 08th, 14th, 15th, 21st and
22nd 2019

Organized by
School of Electrical Engineering,
Vellore Institute of Technology,
Vellore - 632014

Coordinators
Dr. D. Elangovan
Dr. G. Arunkumar

VIT – A Place to Learn; A Chance to Grow



REGISTRATION DETAILS

Participants are requested to register through online by paying the required registration fees (non-refundable) using the below link.

Registration Link:

<http://info.vit.ac.in/Events-VIT/Value-Added-MATLAB/apply.asp>

IMPORTANT DATES

Last Date for Registration: 30th August 2019

Workshop Date: September 08th, 14th, 15th, 21st and 22nd 2019

Registration Fee Details: Rs.300/-

Venue: TT 423.

Course certificate will be awarded on successful completion of course. For registered students the certificate of participation may be considered for additional learning. Participant confirmation will be on first come first serve basis.

TARGET AUDIENCE:

Students of any UG and PG

CONTACT DETAILS

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RESOURCE PERSONS:

Dr. D. Elangovan,
Associate Professor,
SELECT, VIT, Vellore

Dr. G. Arunkumar,
Associate Professor,
SELECT, VIT, Vellore

Last Date: 07th – September - 2019

1. Selection will be made purely on First Come First Serve Basis and Eligibility. (Subject to fulfilling of the seats available).

2. Maximum fifty (50) participants will be accommodated in the course. The Brochure and the Registration Form may be downloaded from the Institute website www.vit.ac.in