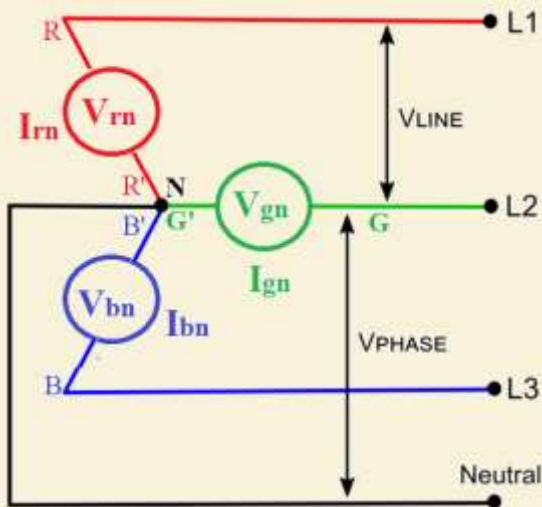


## A Value Added Programme

On

## HANDS ON TRAINING ON CIRCUIT SIMULATION USING ORCAD / MULTISIM

**Dates:** 23-03-2024 to 21-04-2024



### Organized by

School of Electrical Engineering  
Vellore Institute of Technology,  
Vellore-632014.



## ABOUT THE VAP

The main objective of this programme is to enable the participants to have an in depth understanding and hands-on training related to several applications with Orcad/Multisim software tools related to electrical circuit analysis, PCB design and diagnosis. Since the VAP aims at equipping the participants with a wide gamut of tools, the programme spans for a period of 30 hours on 23<sup>rd</sup>, 24<sup>th</sup>, 29<sup>th</sup>, 30<sup>th</sup> March and 21<sup>st</sup> April 2024. This VAP helps student to learn the fundamental simulation of electric networks, simulation of various analog circuits, design of digital & power electronics circuits and electrical machines/apparatus working principles. The outcome of the VAP aims at providing comprehensive overview and understanding of the software tool and its need for the assessment of circuits and its diagnosis.

## TECHNICAL SESSIONS

- Fundamental analysis and simulation of electric networks.
- Simulation of various analog circuits.
- Design and implementation of digital circuits.
- Analysing the power electronics circuits and programming.
- Diagnosis of electrical apparatus/ electrical machines
- Perform hardware experiments on analog and digital circuits

## ABOUT VIT

VIT was established with the aim of providing quality higher education on par with international standards. It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis. The campus has a cosmopolitan atmosphere with students from all corners of the globe. Experienced and learned teachers are strongly encouraged to nurture the students. The global standards set at VIT in the field of teaching and research spurs us on in our relentless pursuit of excellence. In fact, it has become a way of life for us. The highly motivated youngsters on the campus are a constant source of pride. Our Memoranda of Understanding with various international universities are our major strength. They provide for an exchange of students and faculty and encourage joint research projects for the mutual benefit of these universities. Many of our students, who pursue their research projects in foreign universities, bring high quality to their work and esteem to India and have done us proud. With steady steps, we continue our march forward.

## ABOUT THE SCHOOL OF ELECTRICAL ENGINEERING

The School of Electrical Engineering (SELECT) has over 98 faculty members who pursued their UG, PG and Doctoral degrees from top-notch universities. The school offers B.Tech. (Electrical and Electronics Engineering), B.Tech. (Electronics and Instrumentation Engineering), B.Tech (Electrical and Computer Engineering), M.Tech. (Power Electronics and Drives), M. Tech. (Control and Automation), Ph.D and Integrated Ph.D in Engineering. Both B.Tech. and M.Tech. programmes attract the Intelligent students from

the country and abroad. The B.Tech. (Electrical and Electronics Engineering) and B.Tech. (Electronics and Instrumentation Engineering) Programmes are accredited by the Engineering Accreditation Commission of ABET. All UG & PG programmes of the school are accredited by the Institution of Engineering and Technology (IET), U.K. The placement record of the school has always been impressive. Almost 100% of the students secure job from the campus placement and many of them are recruited in core companies. Danfoss Advance Drives Lab, Schneider Electric Smart Energy Monitoring Lab, Fluke Testing and Calibration Lab, Q-Max Automated Test Engineering Lab (Alumni Sponsored Lab) and NxP Semiconductors have established Centre of Excellence for students R&D activities under the guidance of faculty members and industry experts.

### Resource Persons



Dr. S. Balaji,  
SELECT, VIT



Dr. P. Mahalakshmi,  
SELECT, VIT



Prof. R. Santhakumar,  
SELECT, VIT



Dr. R. Mageshvaran,  
SELECT, VIT



Dr. P. Vijayapriya,  
SELECT, VIT

### REGISTRATION

Registration fee : Rs.200/-

\* Registration fee excludes 18% GST.

Prospective participants are requested to register for the VAP through the following web link

<https://events.vit.ac.in/>

### IMPORTANT DATES

Last date for registration: 21-03-2024

Dates of VAP: 23-03-2024, 24-03-2024, 29-03-2024, 30-03-2024 and 21-04-2024

### Co-ordinators

Dr. S. Balaji  
Mobile No. : 9443436702

Prof. R. Santhakumar  
Mobile No. : 9894452792

### ORGANIZING COMMITTEE

#### Chief Patron

Dr. G. Viswanathan  
Chancellor

#### Patrons

Shri. Sankar Viswanathan, Vice President  
Dr. Sekar Viswanathan, Vice President  
Dr. G. V. Selvam, Vice President  
Dr. V.S. KanchanaBhaaskaran, Vice  
Chancellor  
Dr. Partha S. Mallick, Pro-Vice-Chancellor  
Dr. T. Jayabarathi, Registrar

#### Organizing Chair

Dr. Mathew M. Noel,  
Dean, SELECT

Dr. N. Amutha Prabha,  
Associate Dean, SELECT

#### Convenor

Dr. K. Sathish Kumar,  
HoD, Electrical Engineering  
Dr. G.K. Rajini,  
HoD, Instrumentation  
Dr. B. Jaganatha Pandian,  
HoD, Control and Automation  
Dr. P. Ponnambalam,  
HoD, Energy and Power Electronics