# One Day Online Training on "Python Libraries" 08<sup>th</sup> January 2022

**Target Audience** 

UG Students, PG Students and Research Scholars of Vellore Institute of Technology (VIT).

[Internal Participants Only]

#### Venue

The training programme will be conducted in an online platform. The link will be shared to the registered participants.

## **Important Dates**

The interested candidates are instructed to do the online registration and payment on or before 05<sup>th</sup> January 2022.

Registration

A registration fee of INR 250/-(Inclusive of 18% GST) will be charged. Registration shall be done on "First Come First Serve" basis only. Seats are limited to 60. Registration & payment shall be done only by using the following link:

https://events.vit.ac.in/

Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)



Coordinators

Dr. Konguvel E.

Dr. Hariharan I.

Organized by
Department of Embedded Technology
& Micro and Nano Electronics,
School of Electronics Engineering (SENSE),
Vellore Institute of Technology,
Vellore – 632014, India.
www.vit.ac.in

## **Course Content**



Arrays & Matrices in Python



Data\_Analysis with Python



Visualization with Python matpletlib

**Resource Person** 

Shri. B. Mathivanan Senior Consultant, Siqsess Tech. (India) Pvt. Ltd., Chennai, India.



#### **About VIT**

VIT University was started in 1984 as Vellore Engineering College by Dr. G. Viswanathan, Founder & Chancellor, The institute was upgraded to University in recognition of its academic excellence in 2001 by the Ministry of Human Resources Development, Government of India. VIT today comprises of thirteen constituent Schools offering 26 undergraduate, 33 postgraduate and research programs up to PhD level and more than 1400 fulltime faculty members work at VIT. The campus has a cosmopolitan atmosphere with students from all corners of the globe. Experienced and learned teachers are strongly encouraged to nurture the students. The global standards set at VIT in the field of teaching and research spurs us on in our relentless pursuit of excellence. Memoranda of Understanding with various international universities, research organization and industry are the major strengths of VIT. VIT is the first Indian Institute to get international Accreditation from IET and EI, UK. VIT has been retaining the "A" grade from NAAC, VIT is ranked No.1 private engineering Institute by MHRD (NIRF- 2016 ranking). With steady steps, VIT continues to march forward.

### **About SENSE**

School of Electronics Engineering (SENSE) at Vellore Institute Technology was established for imparting state-of-the-art knowledge in Electronics and Communication Engineering and allied areas. B. Tech. Electronics and Communication Engineering is accredited by the Engineering

Accreditation Commission of ABET. All eligible students are placed on campus and many of them are placed in core companies every year. The school has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, Embedded, Sensors and Nanotechnology. Students are encouraged to take up their final year projects abroad too. The School has many industry sponsored advanced laboratories for carrying research and development. MoUs with many foreign Universities, research organizations and Industries allow student and faculty exchange. Faculty are actively involved in R&D activities and are working on research projects funded by government organizations like DRDO, ISRO (RESPOND), BRNS and agencies like DST.

## **About the Programme**

Python is an interpreted highlevel general-purpose programming language. Its language constructs as well as its object-oriented approach aim to help programmers write clear, logical code for small and large-scale projects. Python's large standard library, commonly cited as one of its greatest strengths, provides tools suited to many tasks. As of September 2021, the Python Package Index, the official repository for third-party Python software, contains over 329,000 packages with a wide range of functionality including Automation, Data Analytics, Graphical User Interfaces, Machine Learning, Mobile & Web applications, Multimedia, Networking and Scientific Computing.

# **Advisory Committee**

Patron

Dr. G. Viswanathan, Chancellor

Co-Patrons

Shri. Sankar Viswanathan, VP
Dr. Sekar Viswanathan, VP
Shri. G.V. Selvam, VP
Dr. Rambabu Kodali, VC
Dr. S. Narayanan, Pro-VC
Dr. K. Sathiyanarayanan, Registrar

Advisors

Dr. Sivanantham S.

Professor & Dean, SENSE

**Dr. Jasmin Pemeena Priyadarisini** Professor & Associate Dean, SENSE

Dr. P. Sasikumar Associate Professor & Head, Dept. of Embedded Technology, SENSE

Dr. S. Kumaravel
Associate Professor & Head,
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