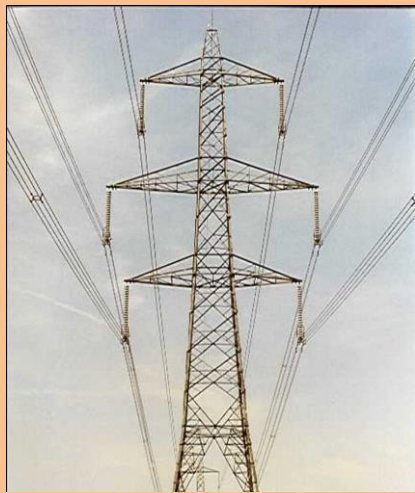




VIT[®]
Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)

**Value Added Programme
on**

Exploring MATLAB/ Simulink



Organized by

**School of Electrical Engineering
(SELECT), VIT, Vellore – 632014**

ABOUT VIT

Vellore Institute of Technology (VIT) was founded in 1984 as Vellore Engineering College by Chancellor Dr. G.Viswanathan. VIT was established with the aim of providing quality higher education on par with international standards. The credentials of VIT in academics and research, has placed VIT in the 9th position as a University 10th in research and 12th in Engineering in India by NIRF, Govt. of India Ranking. The QS world ranking has given 4 STAR rating to VIT, with that VIT becomes the first institution in India to have the 4 STAR rating. In addition to this, the consortium of industries, FICCI has adjudged VIT as the “Excellence in Faculty”. VIT has the record of publishing a maximum number of SCOPUS Indexed Research Journal papers, among Indian Universities, overtaking all the premier institutions. VIT has also completed 4 cycles of NAAC accreditation and has been rated as “A++” grade institution. VIT is No.1 private university for Innovation (2019) as recognized by ARIIA, Government of India. VIT has introduced many innovations in academic processes like Fully Flexible Credit System, Project Based Learning for better learning, fully digitized academic portals that assist students in equipping themselves for 2025 market-place.

ABOUT SELECT

The School of Electrical Engineering (SELECT) is a part of VIT since its inception. The School has grown tremendously over years and is now recognized as one of the major engineering schools in VIT. The School has 98 faculty members from various reputed institutes. Besides high-quality teaching and instruction at both UG and PG levels, the faculty members of the school are actively involved in executing a number of R&D and consultancy projects from government agencies including Royal Academy-UK, DST, ISRO, DRDO and also from many reputed industries.

ORGANIZING COMMITTEE

Chief Patron

Dr. G. Viswanathan, *Chancellor*

Patrons

Shri. Sankar Viswanathan, *Vice President*

Dr. Sekar Viswanathan, *Vice President*

Shri. G. V. Selvam, *Vice President*

Dr. Kanchana Baaskaran, *VC*

Dr. Partha Sharathi Mallick, *Pro-VC*

Dr. T. Jayabarathi, *Registrar*

Organizing Chair

Dr. Mathew Mithra Noel, *Dean, SELECT*

Dr. N. Amutha Prabha, *Asso. Dean, SELECT*

Co-organizing Chair

Dr. K. Sathish Kumar, *HoD, EEE*

Dr. P. Ponnambalam, *HoD, EPE*

Dr. B. Jaganathapandian, *HoD, C&A*

Dr. G. K. Rajini, *HoD, E&I*

Convener

Dr. C. Rani, Professor, EEE

Dr. Brisilla, Professor, PED

Dr. Sankardoss, Assistant Professor, PED

TARGET GROUP

Research Scholars, Students and Corporate Practitioners can attend.

DATE & TIME

8th, 15th, 16th, 22nd September and 2nd October 2024

10.00 AM -1.00 PM and 2.00 PM-5.00 PM

OBJECTIVES OF THE VALUE-ADDED PROGRAMME

• MATLAB/Simulink is a simulation software specially designed for all engineering disciplines. With fast simulation speed and friendly user interface. MATLAB/Simulink is a powerful simulation environment to meet the simulation and development needs. It provides a complete platform from design to simulation. to hardware implementation.

In addition, MATLAB/Simulink provides separate toll boxes for Power Systems, Image Processing and Machine learning. This value-added program can provide knowledge and hands on training for the UG/PG students and research scholars on design of feedback

controllers and state observer, modelling of electrical machines, design of control strategies for Motors. Optimization techniques, image processing and machine learning applications using MATLAB/Simulink..

RESOURCE PERSONS

1. Dr. Rani Chinnappa Naidu, VIT, Vellore
2. Dr. R M Brisilla, VIT, Vellore
3. Dr. Sankardoss, VIT, Vellore

REGISTRATION FEE

- ❖ Registration fee : Rs.200/-
- Registration fee includes 18% GST.
- Prospective participants are requested to register for the VAP through the following web link
- <https://events.vit.ac.in/>
- The number of participants is **strictly limited to 50** based on first come first serve.
- Certificate will be issued to all registered and attended participants.
- Attendance is mandatory for all sessions

LAST DATE OF REGISTRATION

8th September 2024

CONTACT

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