



School of Electronics Engineering

Department of Micro and Nano electronics

Value Added Course

On

Hands - on Analog and Digital IC Design using Cadence Tools –VAC

About the Course: The value added course on “**Hands on Analog and Digital IC Design using Cadence Tools**”, aim to introduce the most demand skills in VLSI. This course deals with the fundamental theory to Practical hand on training in vlsi. This course allows the student to learn from the scratch to write code , design using the CMOS transistors which help them to manage their projects efficiently and make the career as a vlsi design engineer.

Advisory Committee

Dr. Sivanantham S, Professor & Dean, School of Electronics Engineering

Dr. Prakasam P, Professor & HOD
Department of Communication Engineering, SENSE

Dr.Kumaravel, Professor & HOD
Department of Micro and Nano electronics, SENSE

Faculty Coordinators:

Dr. S Ravi, SENSE
Email: msravi@vit.ac.in

Dr. Satheeshkumar S , SENSE,
satheeshkumar.s@vit.ac.in

Dr.Rajeev Pankaj Nelapaty,SENSE
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Highlight of the Course:

- 35 hours Course Duration
- Introduction to Verilog & CMOS
- Practical training: How to write code using Verilog HDL Programming.
- Practical training: How to design Semicustom VLSI circuits using Cadence tools.
- Mini Project based on 45nm node
- E Certificate
(Min of 75% attendance & 60% of marks)

Course Experts

Industry: Mr. Suprovab Mandal, Senior Engineer,
Qualcomm. Malaysia

Faculty: Dr. Ravi S. SENSE

Dr. Satheesh kumar . SENSE

Dr.Rajeev Pankaj, SENSE

Alumni: Alumni Industry Engineers

Target Audience: UG / PG

Seats Limited to 100 Participants

- **Date:** 20.3.23(6.00pm-9.00pm) & 25.3.23 (9.30am -6.30pm)
27.3.23(6.00pm -9.00pm) & 01.4.23 (9.30am -6.30pm)
29.3.23(6.00pm- 9.00pm) & 8.4.23 (9.30am -6.30pm)

Note: Saturdays – Offline classes - TT238 Other classes on MS Teams

Registration Fee: Rs 885 (Inclusive of GST) **Registration Link** <https://events.vit.ac.in/>

Contct Numbers

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