



## School of Electronics Engineering

Department of Micro and Nano electronics

### Value Added Course

On

## Hands on Analog and Digital IC Design using Cadence Tools –VAC - 1555

**About the Course:** The value added course on “**Hands on Analog and Digital IC Design using Cadence Tools**”, aim to introduce the most demanding skills in VLSI domain. This course deals with the fundamental theory to Practical hands-on training in vlsi. This course allows the student to learn Analog and Digital IC Design flow, which help them to manage their projects efficiently and make the career as a VLSI design.

### Highlight of the Course:

- 30 hours Course Duration
- Introduction to Verilog & CMOS
- Practical training: How to write code using Verilog HDL Programming.
- Practical training: How to design Semicustom Analog and Digital IC Design circuits using Cadence tools.
- Mini Project based on available Lower nm node
- E Certificate

### Course Experts:

#### Faculty:

Dr.Harish M Kittur  
Dr. Ravi S. SENSE  
Dr. Satheesh Kumar. SENSE  
Dr.Rajeev Pankaj, SENSE

### Advisory Committee

Dr. Sivanantham S, Professor & Dean, School of Electronics Engineering (SENSE)

Dr. Noor Mohammed, Professor & HOD  
Department of Communication Engineering, SENSE

Dr. Jagannadha Naidu K  
HOD ,  
Department of Micro and Nano electronics, SENSE

### Faculty Coordinators:

Dr. Satheesh kumar S , SENSE,  
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Dr. S Ravi, SENSE  
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Dr.Rajeev Pankaj Nelapati, SENSE  
[rajeevpankaj@vit.ac.in](mailto:rajeevpankaj@vit.ac.in)

**Target Audience:** UG / PG / PhD

**Seats Limited to 60 Participants**

**Date: 14.9.24 (9.00am-7.00pm)**

**15.9.24 (9.00am-7.00pm)**

**16.9.24 (9.00am-7.00pm)**

**Registration Fee:** Rs 1180 (Inclusive of GST) **Registration Link :** <https://events.vit.ac.in/>

### Contct Numbers

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