Bachelor of Technology in Electronics Engineering (VLSI Design & Technology) School of Electronics Engineering

Programme Credit Structure		(Cre	edits		Technical Report Writing Quantitative Skills Practice I	0	0		1 1.5
Foundation Core Courses			,	19		Quantitative Skills Practice II	-	0		1.5
Basic Sciences and Mathematics				+9 24			0			
						Qualitative Skills Practice I	0	0		1.5
Engineering Sciences				10		Qualitative Skills Practice II	0	0		1.5
Humanities, Social Sciences and			_			Foreign Language	2	0	0	
Management (HSM)				15	BHSM200L	HSM Elective	3	0	0	3
Discipline-linked Engineering Science Cour	ses	;		10						
Discipline Core Courses				53	Discipline-li	nked Engineering Science Cour	ses			10
Discipline Elective Courses				15	2.00.p0	g colones cou	-			. •
Open Elective Courses			•	15	BEVD101L	Electronic Materials	3	0	0	3
Project and Internship			()9		Physics of Semiconductor De-		0		
Total Graded Credit Requirement			1	51		vices		·	Ū	Ū
Non-Graded Credit Requirement			•	11		Circuit Theory	3	1	0	4
Basic Sciences and Mathematics				24	Discipline C	Core Courses				53
	L	т	Р	С	Discipline 0	ore courses				50
BPHY101L Engineering Physics	3				BECE102L	Digital Systems Design	3	0	0	3
BPHY101P Engineering Physics Lab			2				0		2	
BCHY101L Engineering Chemistry			0			Microprocessors and Microcon-	3		0	
BCHY101P Engineering Chemistry Lab			2			trollers	Ū	Ū	Ū	Ū
BMAT101L Calculus			0			Microprocessors and Microcon-	Ο	0	2	1
BMAT101P Calculus Lab			2			trollers Lab	U	U	_	•
BMAT1011 Calculus Lab BMAT102L Differential Equations and	3					Control Systems	2	1	Λ	3
Transforms	3	'	U	4		VLSI System Design	3		0	
	0	4	^	1		VLSI System Design Lab	0		2	
BMAT201L Complex Variables and Linear	3	ı	U	4		· · · · · · · · · · · · · · · · · · ·	-		0	
Algebra	_	^	^	_		,	3			
BMAT202L Probability and Statistics	3					0	3		0	
BMAT202P Probability and Statistics Lab	0	U	2	1			3		0	
							0		2	
Engineering Sciences				10		Scripting Languages and Verification			0	
BEEE102L Basic Electrical and Electronics Engineering	3	0	0	3		Scripting Languages and Verification Lab	0	0	2	1
BEEE102P Basic Electrical and Electronics	0	0	2	1	BEVD206L	Semiconductor Device Mod-	2	0	0	2
Engineering Lab				-		elling				
BCSE101E Computer Programming: Python	1	0	4	3	BEVD206P	Semiconductor Device Mod-	0	0	2	1
BCSE103E Computer Programming: Java	1					elling Lab				
BOOL TOOL Computer Frogramming.oava	•	Ü	7	U		Computer Architecture	3	0	0	3
						ASIC Design				3
Humanities, Social Sciences and Manageme	ent			15		ASIC Design Lab	0			1
DENOTOTAL ER LE	^	_		•		Principles of Communication	-	0		
BENG101N Effective English Communication (NGC)	0	O	4	2		Systems				
BENG101L Technical English Communica-	2	0	0	2		CAD for IC Design	3		0	
tion						CMOS Analog IC Design	3			3
BENG101P Technical English Communica-	0	0	2	1		CMOS Analog IC Design Lab	0		2	
tion Lab	-					VLSI Technology	3			3
					BEVD305P	VLSI Technology Lab	0	0	2	1

Discipline Elective Courses			15			Open Elective Courses	1:	
	2D Materials and Devices		0			Engineering Disciplines Projects Sciences Hui	mani	
	Compound Semiconductors		0			ties Social Sciences Liberal Arts Economics Fina	ance	
BEVD210L	Quantum Technology for Electronics Engineers	3	0	0	3	Entrepreneurship Management Skills Reading		
BEVD211L	Thin Films Characterization	3	0	0	3			
	Photovoltaics and Energy Conversion Devices	3	0	0	3	Project and Internship	9	
BEVD307L	Memory Devices and circuits	3	0	0	3	BEVD399J Summer Industrial Internship	1	
	Low Power VLSI Design		0			BEVD497J Project-I	3	
	Testing of VLSI Circuits		0			BEVD498J Project-II / Internship	5	
	Embedded System Design with		0			BEVD499J One Semester Internship	1	
DEV/D0111	FPGA	2	0	^	2	Non-Graded Credit Requirement	1	
DEVUSITE	VLSI Digital Signal Processing Systems	3	U	U	3	DEVIDAGAN Later Letter to English to		
BEVD313I	Advanced Computer Architec-	2	0	Λ	2	BEVD101N Introduction to Engineering		
DL V D3 12L	ture	3	U	U	3	BSSC101N Essence of Traditional Knowl- edge	Ž	
	Computational Techniques	2	1	0	3	BSSC102N Indian Constitution	1	
BEVD314L	Mixed Signal Circuit Design	3	0	0	3	BEXC100N Extracurricular Activities	1	
BEVD315L	CMOS RF IC Design	3	0	0	3	BCHY102N Environmental Sciences	1	
	Electronics Packaging	3	0	0	3	BHUM101N Ethics and Values	:	
BEVD402L	Al and Machine Learning for IC	3	0	0	3			
BECE403L	Neuromorphic Computing	3	0	0	3			
BECE404L	Design of CMOS Phase Locked Loop	3	0	0	3			
BECE405L	Hardware Security	3	0	0	3			
BECE406L	High Speed Interconnects for VLSI Design	3	0	0	3			
BECE407L	Stacked IC Design	3	0	0	3			
	Verification Methodologies	3	0	0	3			
BECE409L	Advanced Semiconductor De-	3	0	0	3			
	vices							
BECE410L	Introduction to MEMS	3	0	0	3			
BEVD391J	Technical Answers to Real Prob- lems Project				3			
BEVD392J	Design Project				3			
	Laboratory Project				3			
	Product Development Project				3			
	Reading Course				3			
	Special Project				3			
	Simulation Project				3			
	•							